000280 SCHT5960

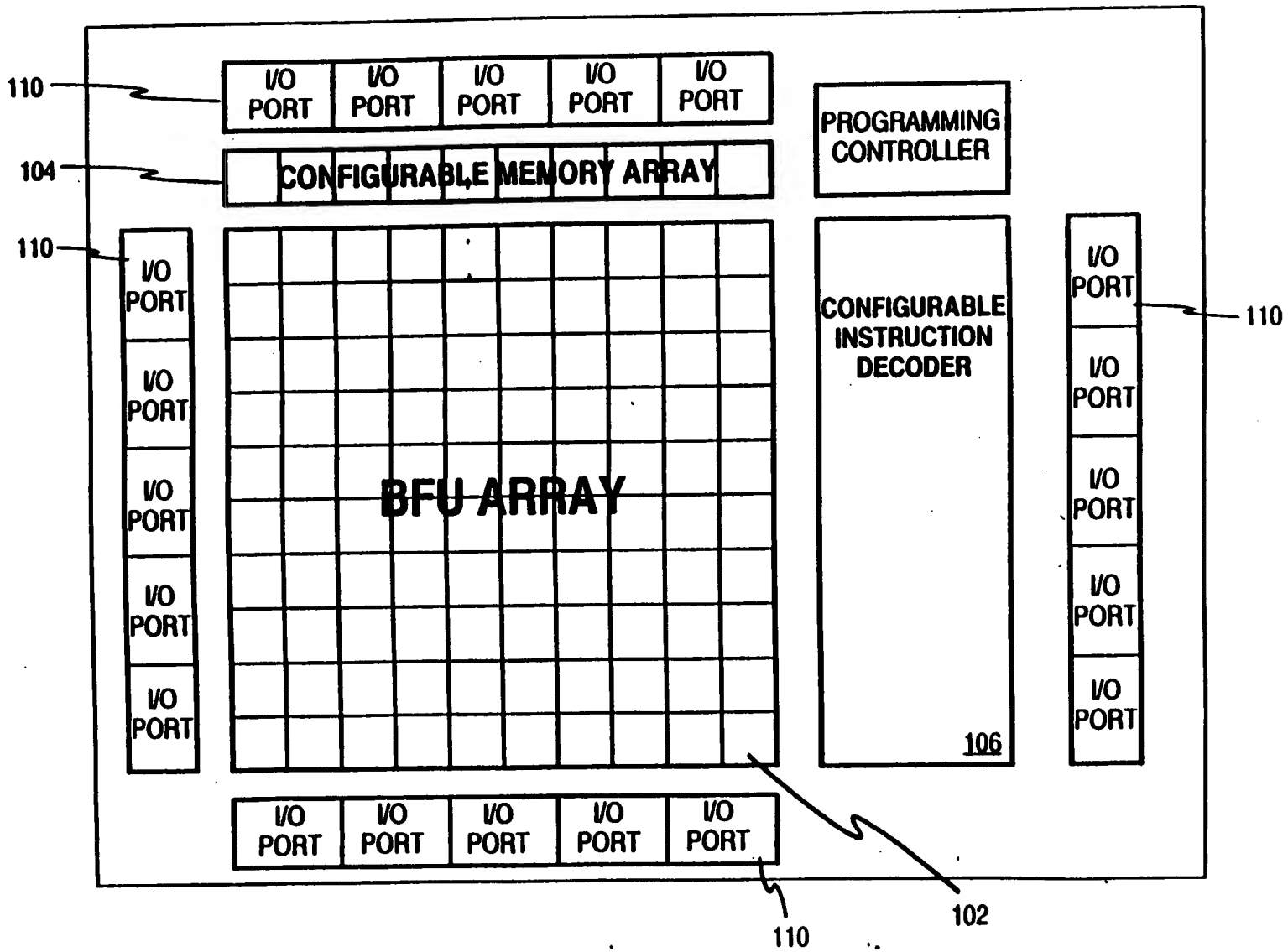

FIG. 1

000280 Sch1S960

MCPE

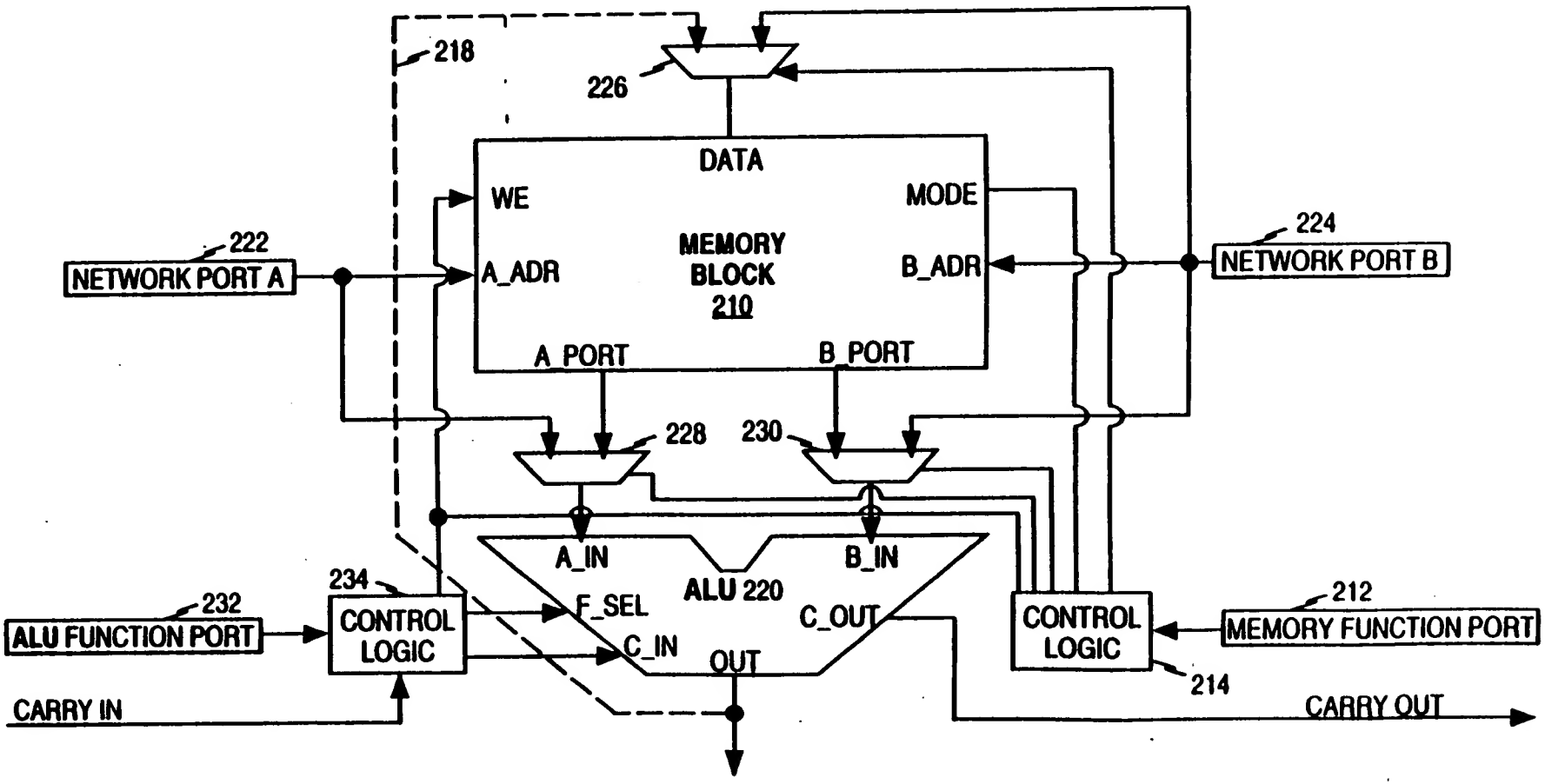

FIG. 2

000680 "5214960

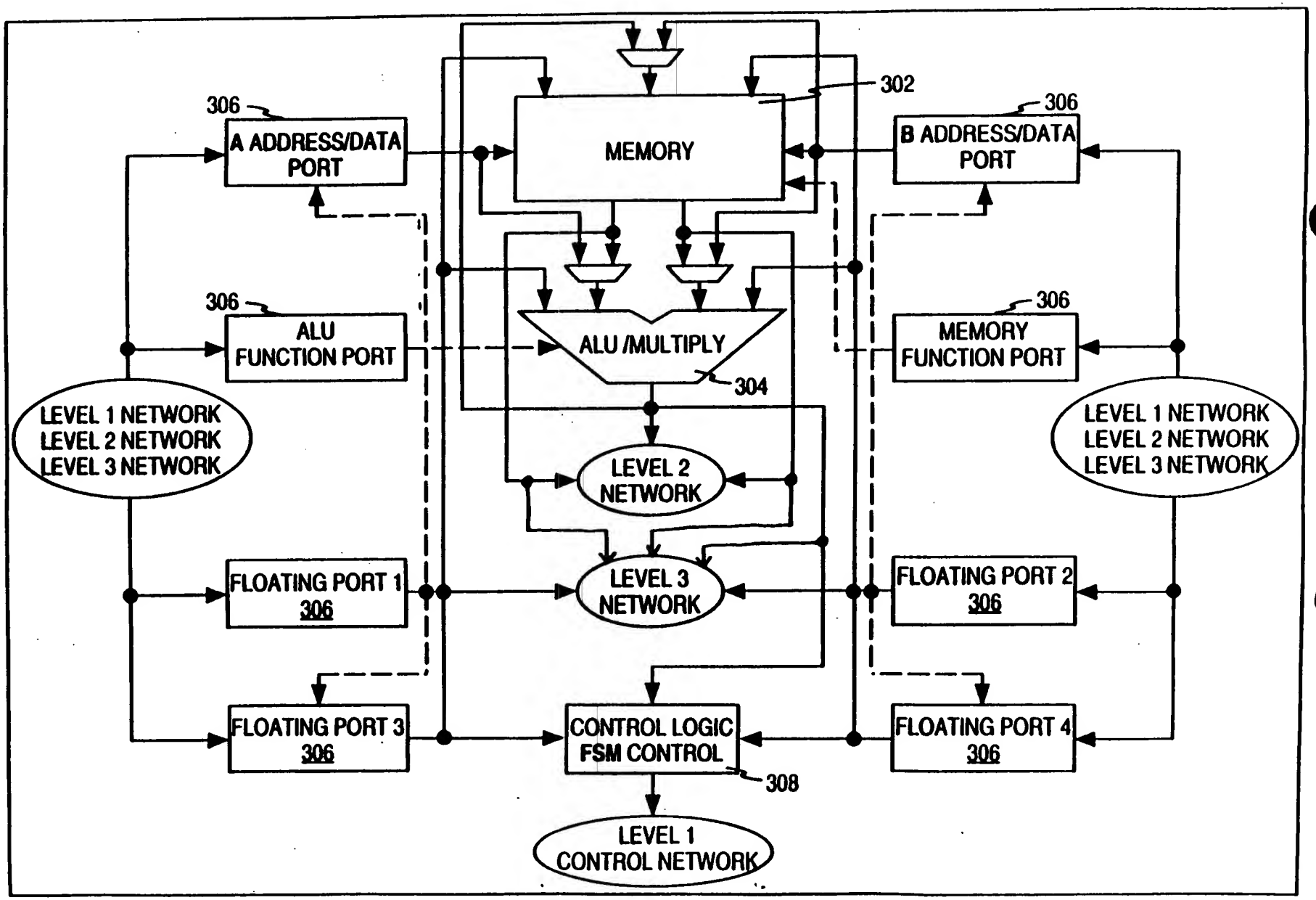

FIG. 3

000260 "SEHT5960

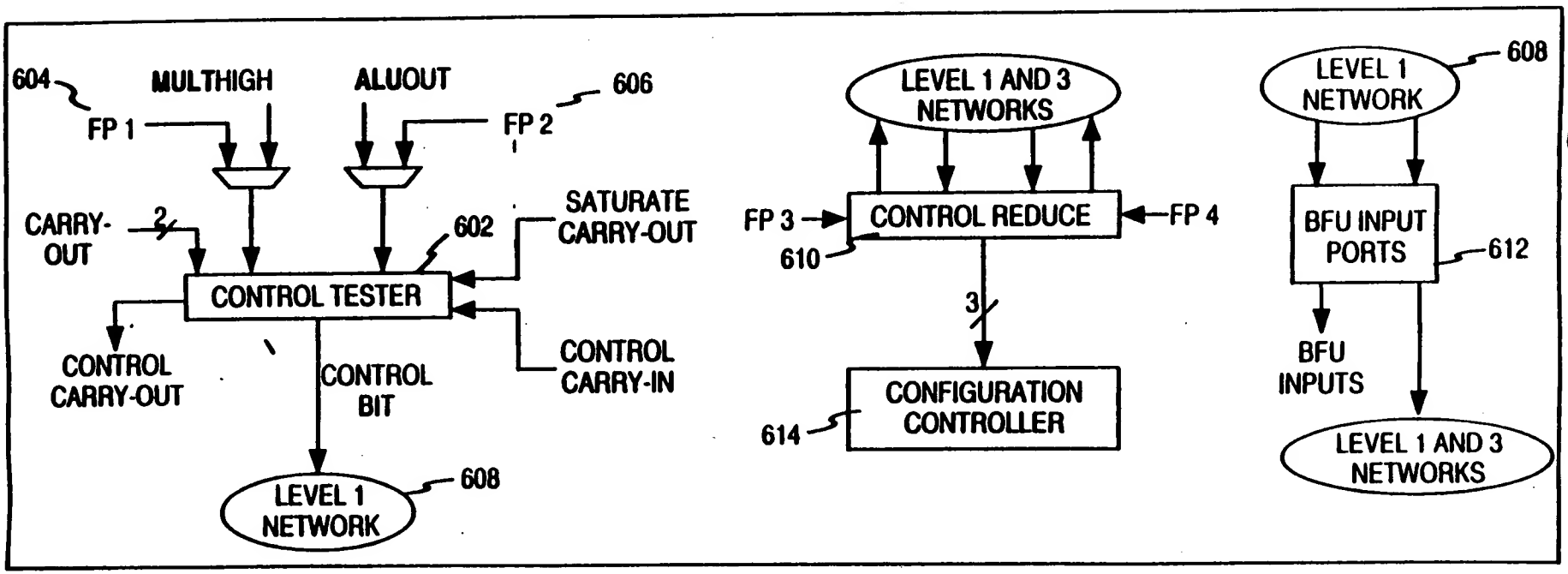

FIG. 4

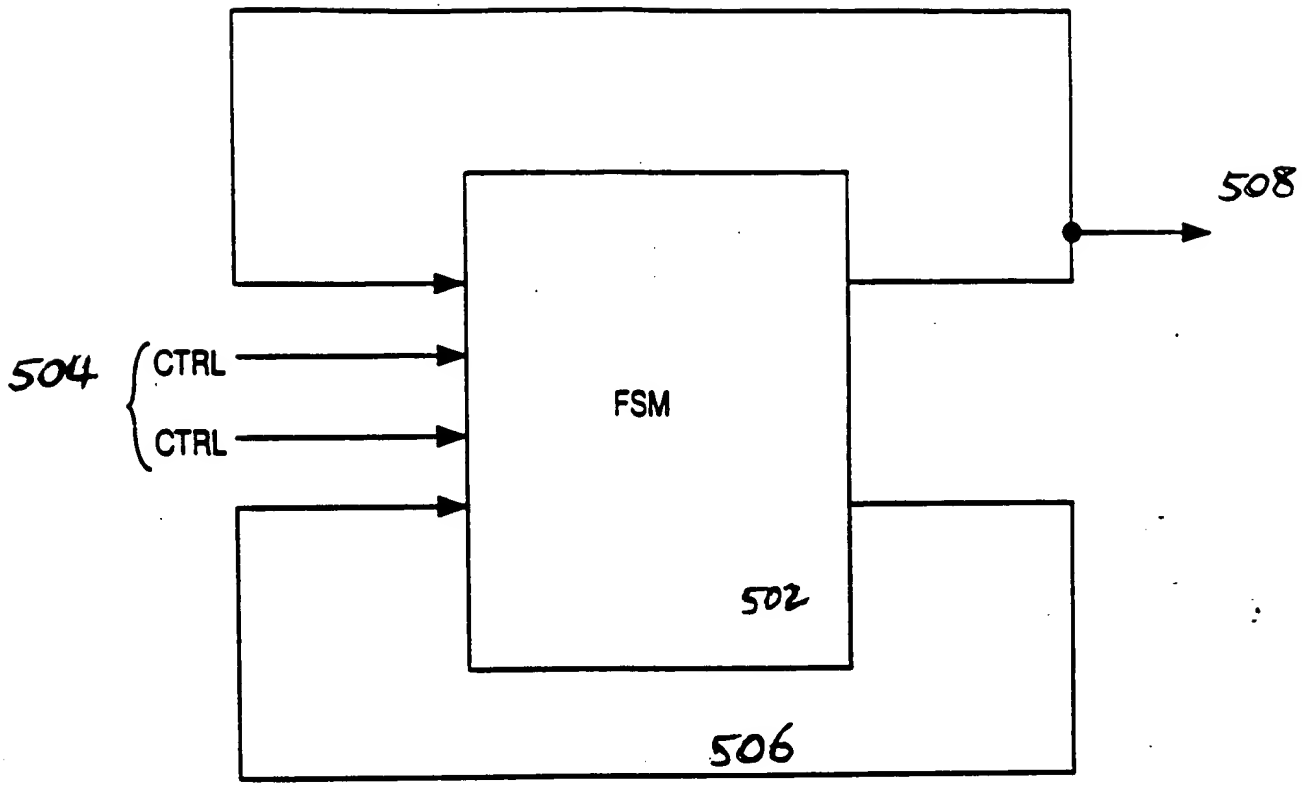

FIG. 5

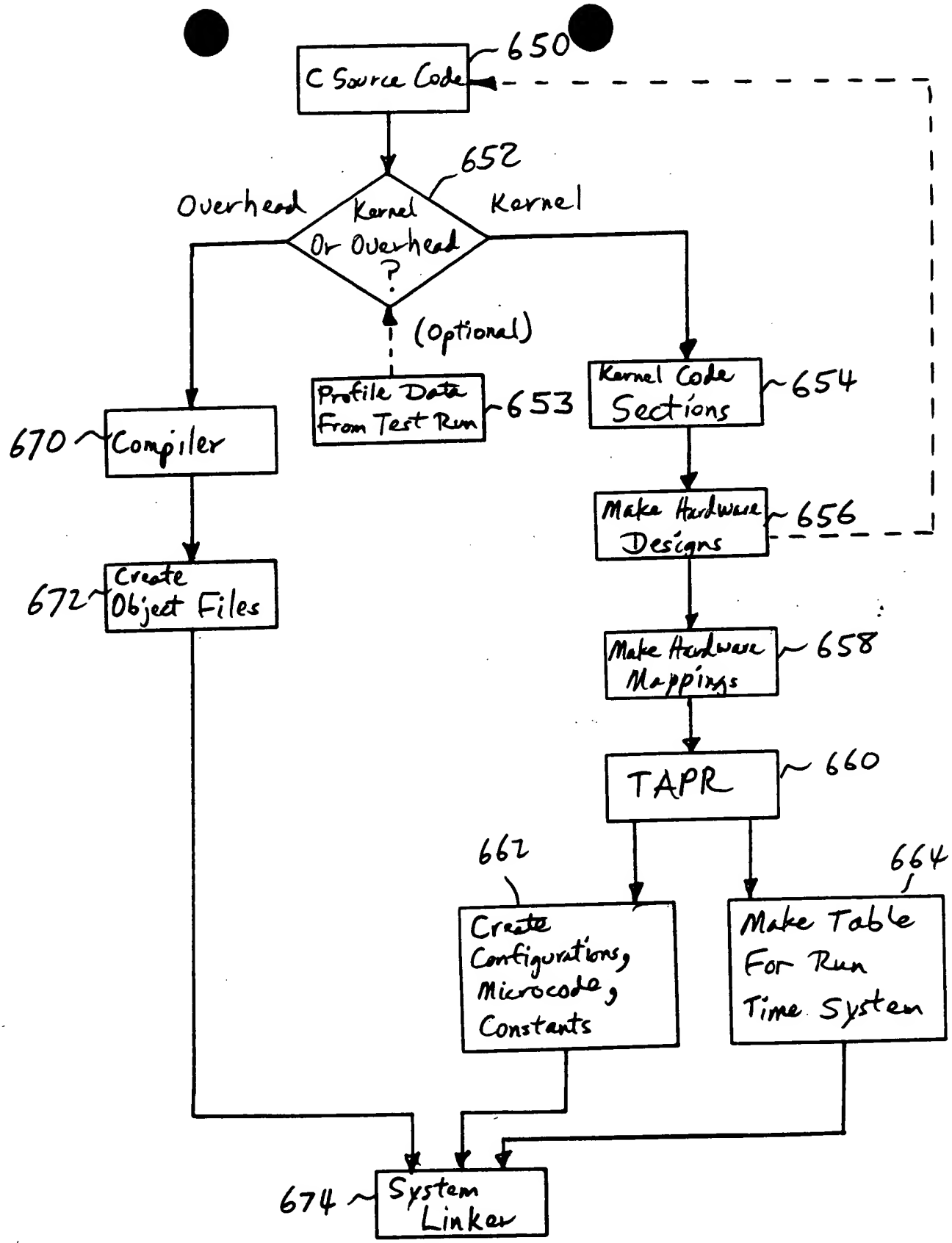
650
C Source Code
652
Overhead
Kernel Or Overhead ?
Kernel
(Optional)
Profile Data From Test Run
653
Kernel Code Sections
654
670
Compiler
Make Hardware Designs
656
672
Create Object Files
Make Hardware Mappings
658
TAPR
660
662
664
Create Configurations, Microcode, Constants
Make Table For Run Time System
674
System Linker

FIGURE 6

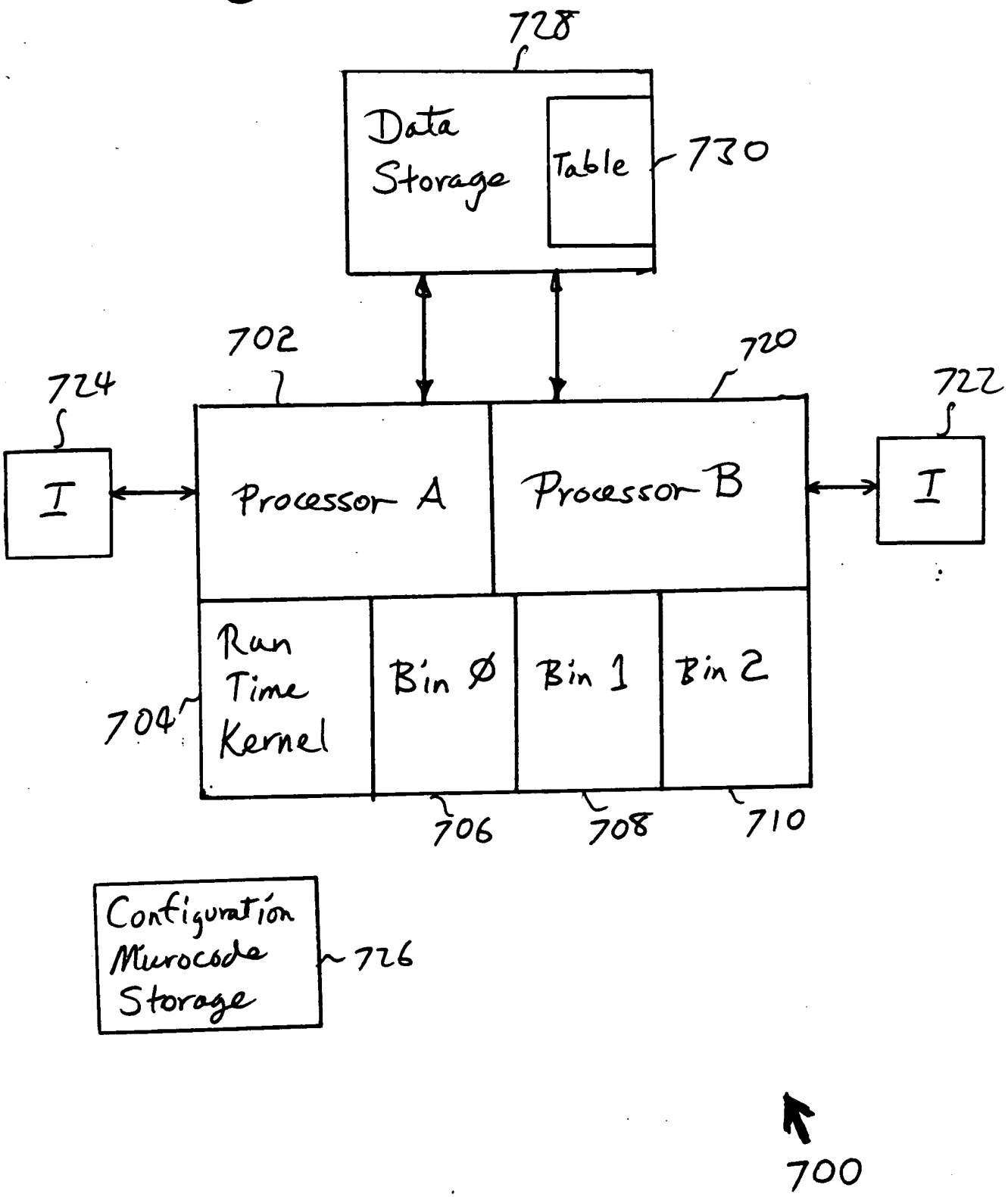
728
Data Storage
Table
730
702
720
724
722
I
Processor A
Processor B
I
Run Time Kernel
704
Bin Ø
Bin 1
Bin 2
706
708
710
Configuration Microcode Storage
726
700

FIGURE 7A

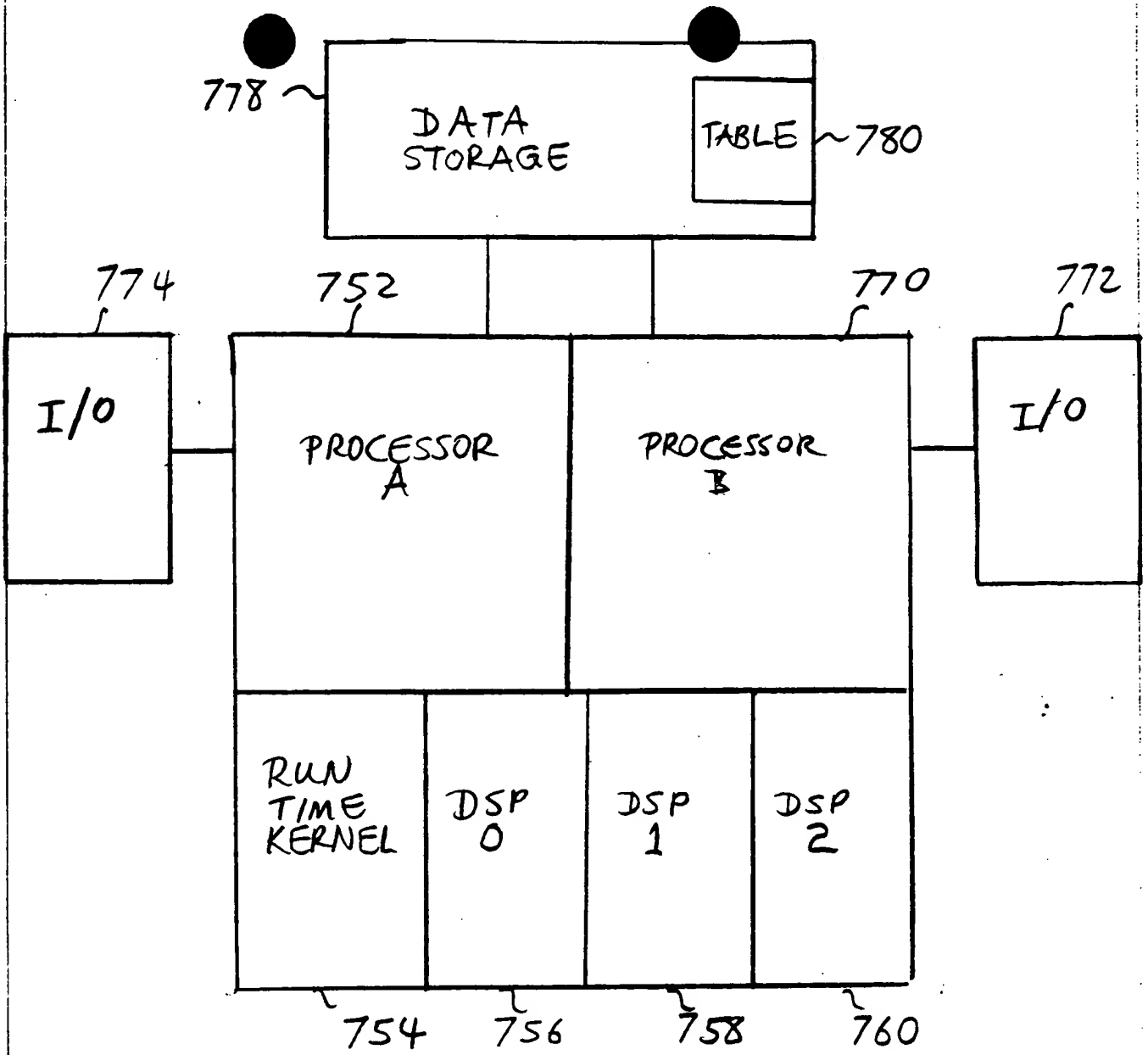
778
DATA STORAGE
TABLE
780
774
752
770
772
I/O
PROCESSOR A
PROCESSOR B
I/O
RUN TIME KERNEL
DSP 0
DSP 1
DSP 2
754
756
758
760
750

FIG 7B

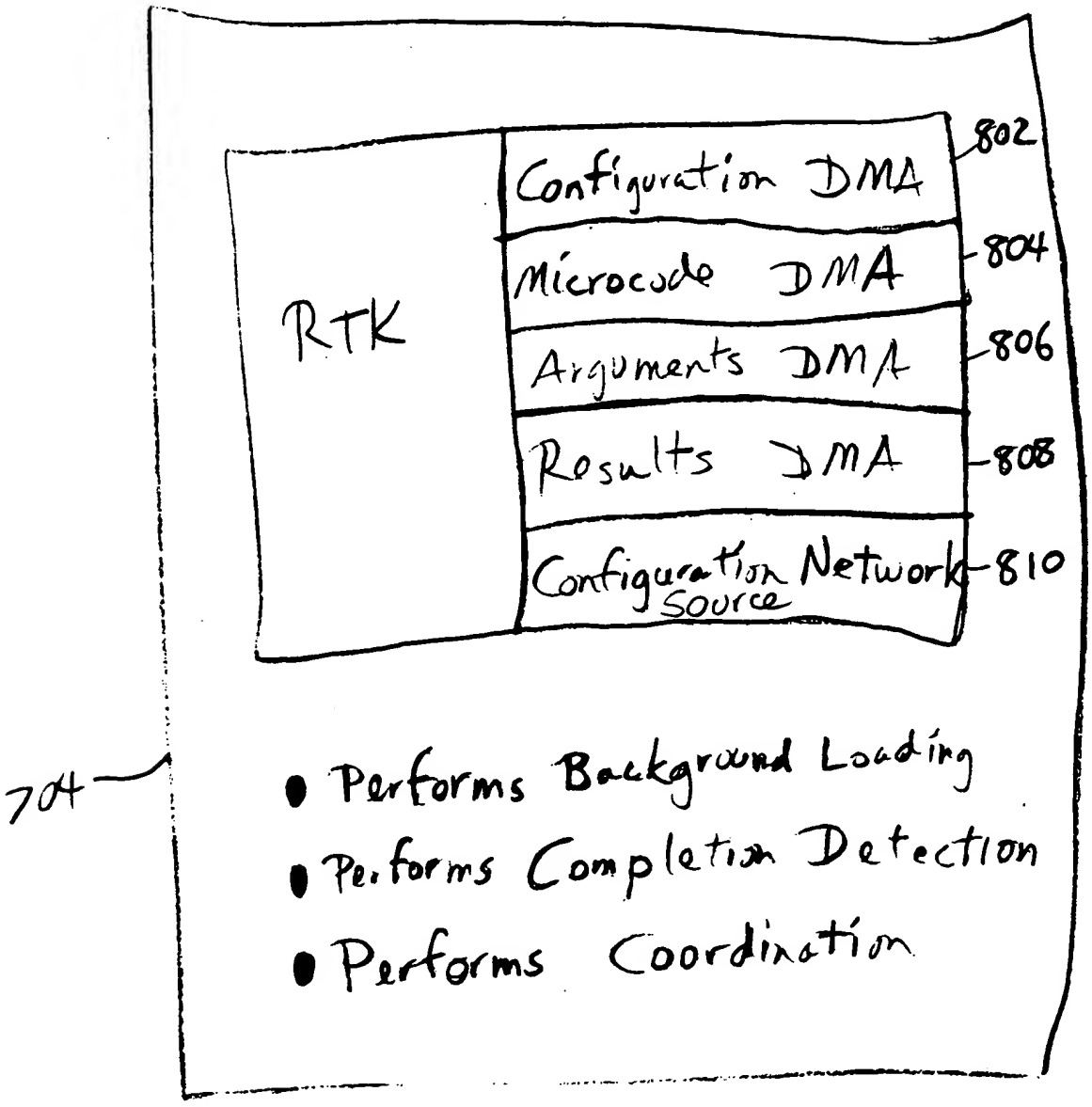
RTK
Configuration DMA
802
Microcode DMA
804
Arguments DMA
806
Results DMA
808
Configuration Network Source
810
704
• Performs Background Loading
• Performs Completion Detection
• Performs Coordination

FIG. 8

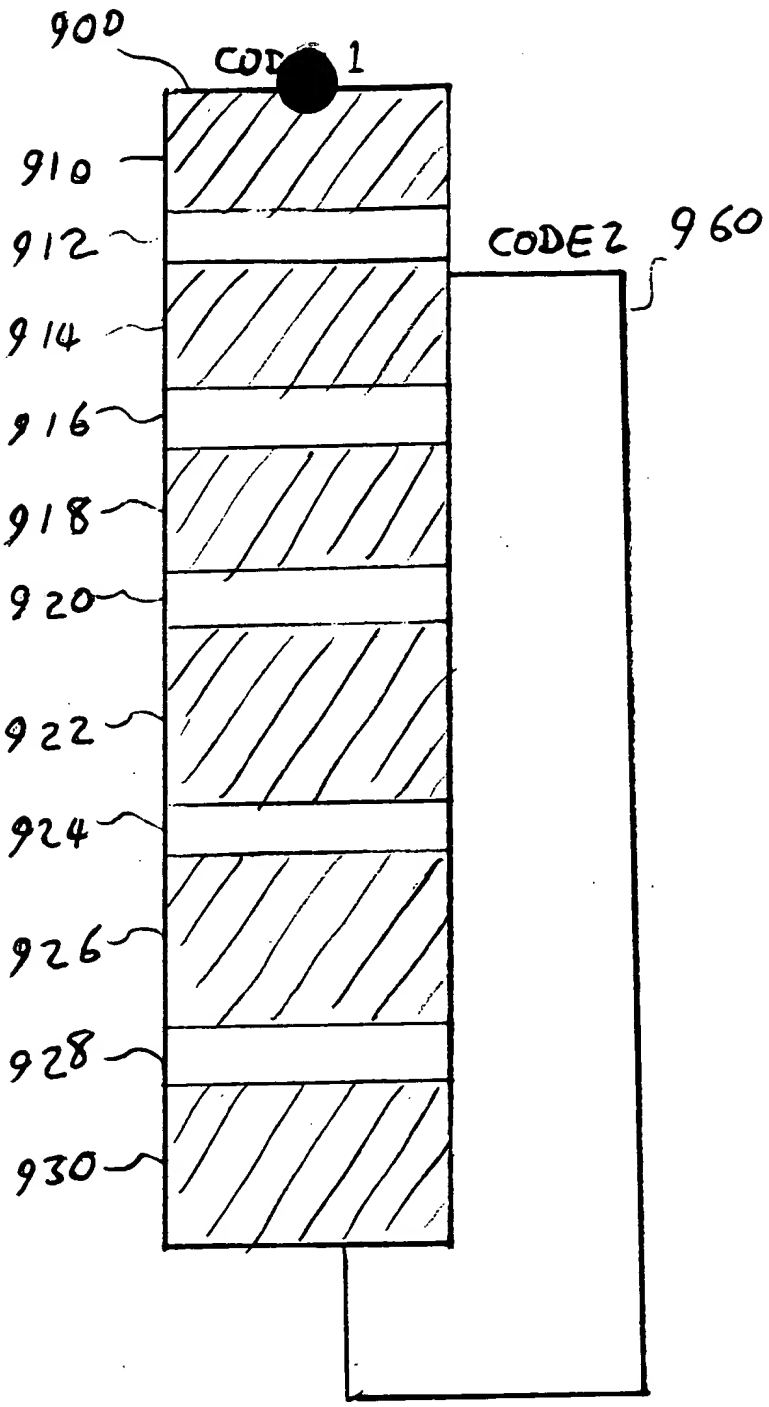

FIG.9A

000280 "SEARCH"

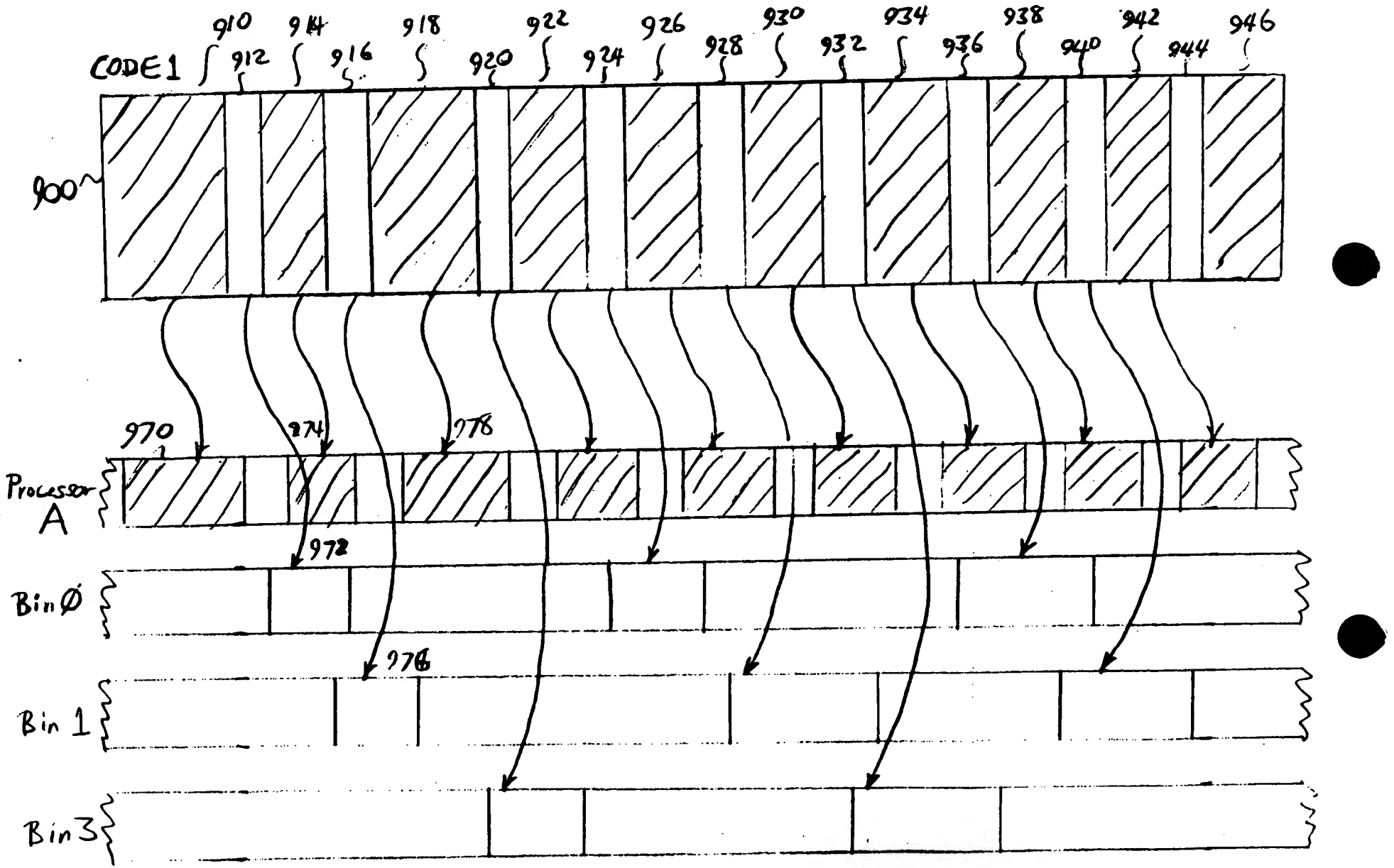

FIG 9B

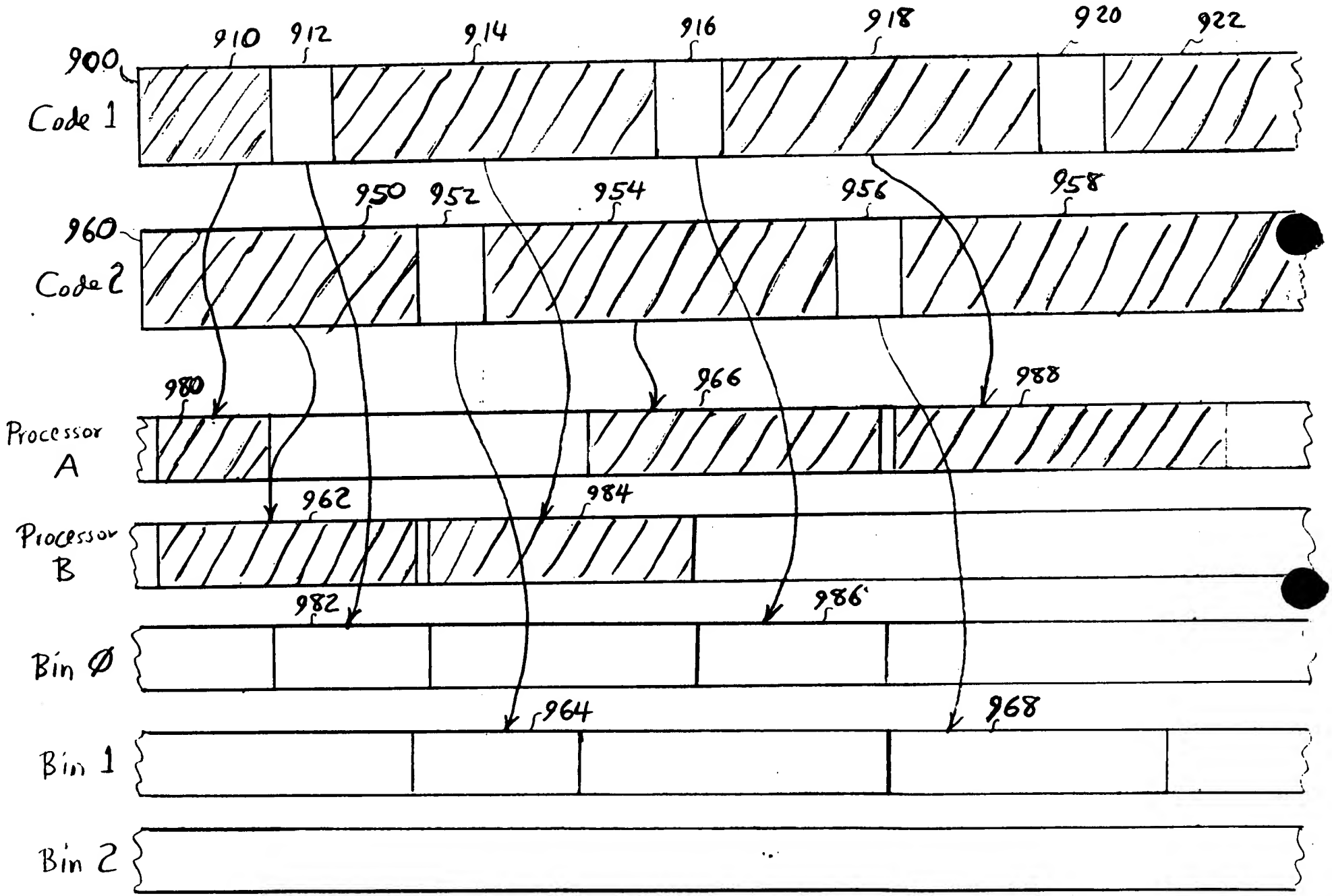
900
910
912
914
916
918
920
922
Code 1
960
950
952
954
956
958
Code 2
980
966
988
Processor A
962
984
Processor B
982
986
Bin Ø
964
968
Bin 1
Bin 2

FIG. 9C

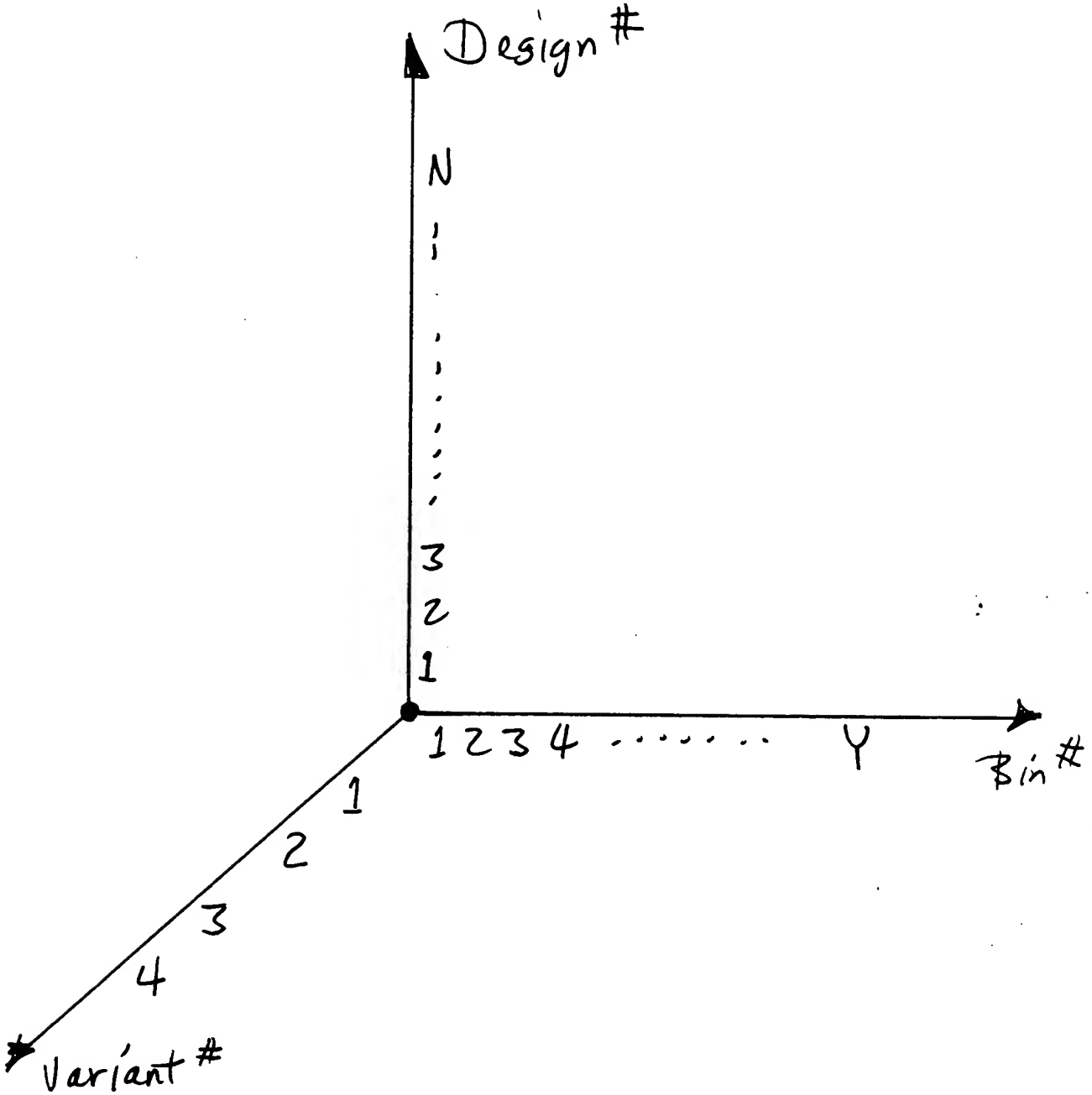

FIGURE 10

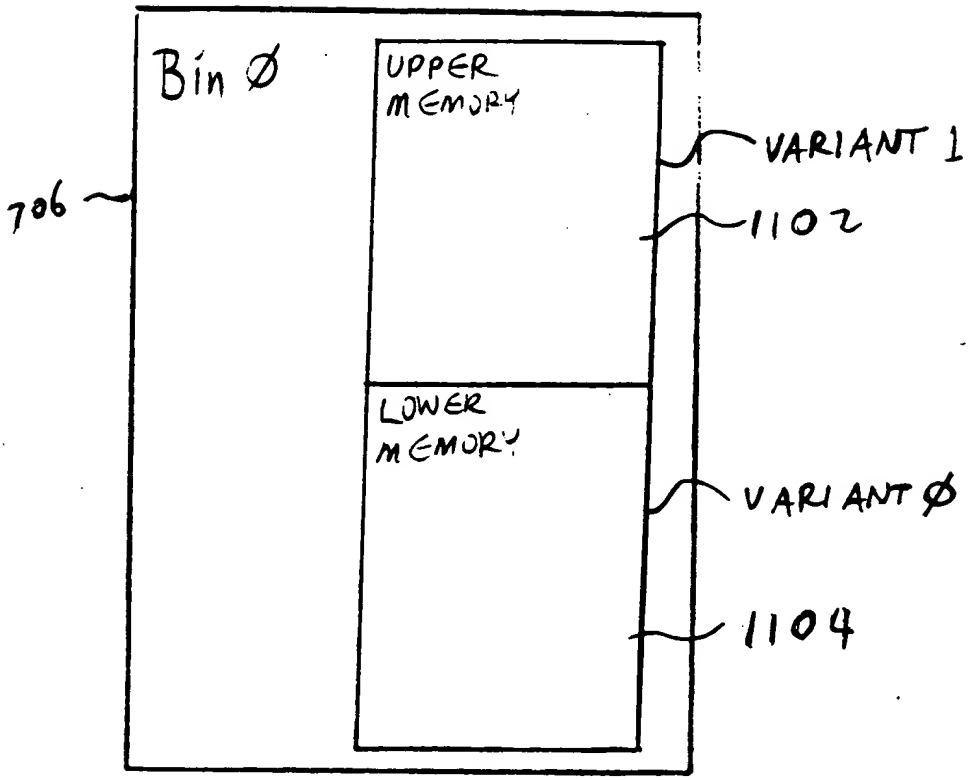
Bin Ø
UPPER MEMORY
LOWER MEMORY
706
VARIANT 1
1102
VARIANT Ø
1104

FIG. 11

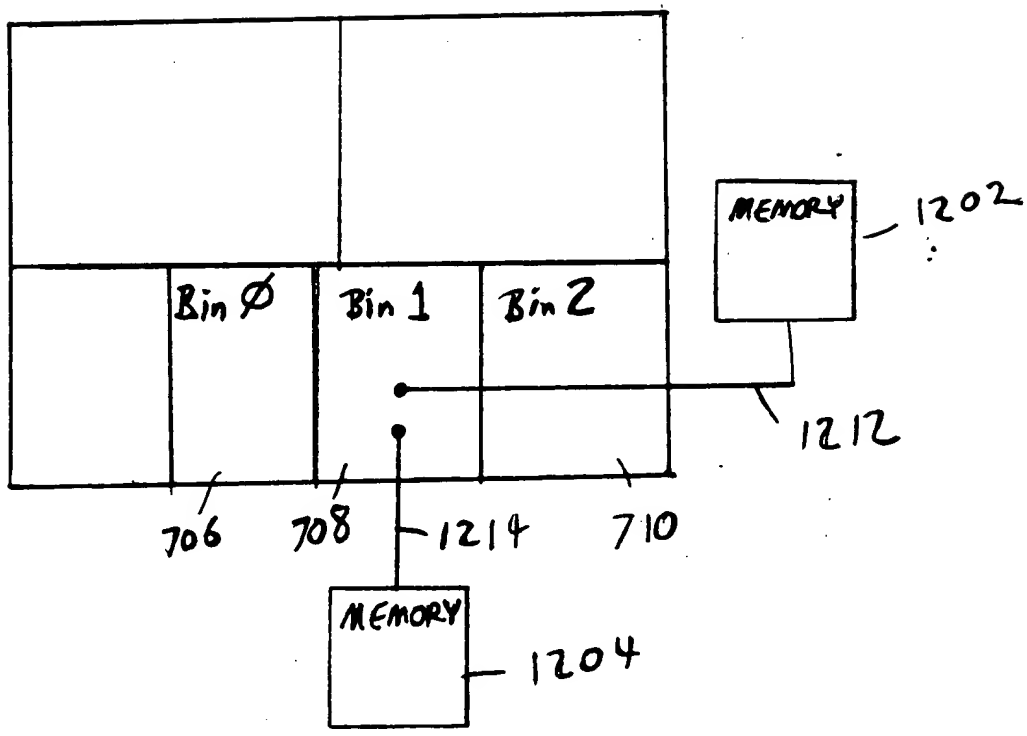

FIG. 12

000260 Sch1SSSU

AMPAD 22-141 50 SHEETS
22-142 100 SHEETS
22-144 200 SHEETS

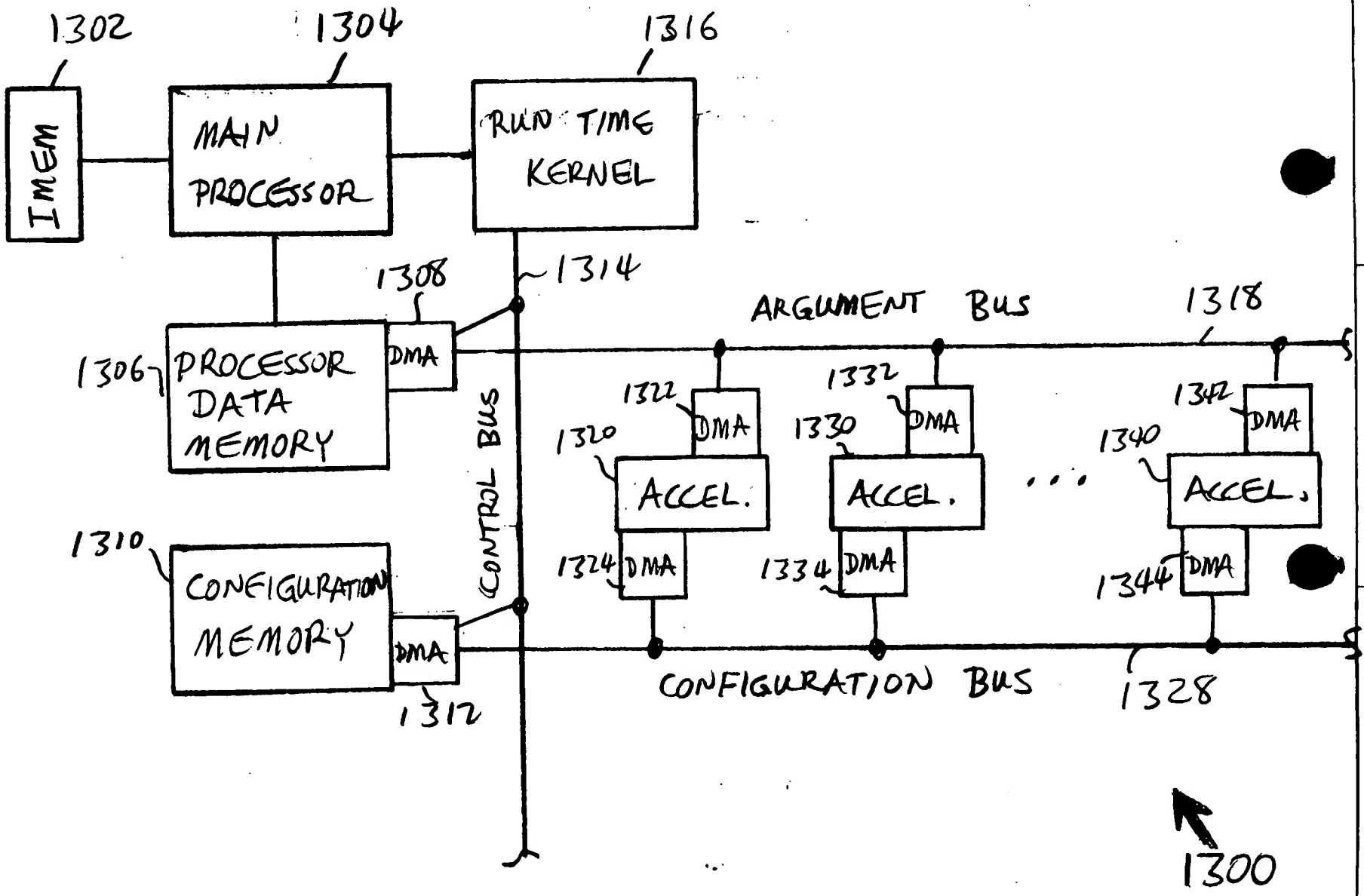

FIG 13

000280 SEMI5960

AMPAD 22-141 50 SHEETS 22-142 100 SHEETS 22-144 200 SHEETS

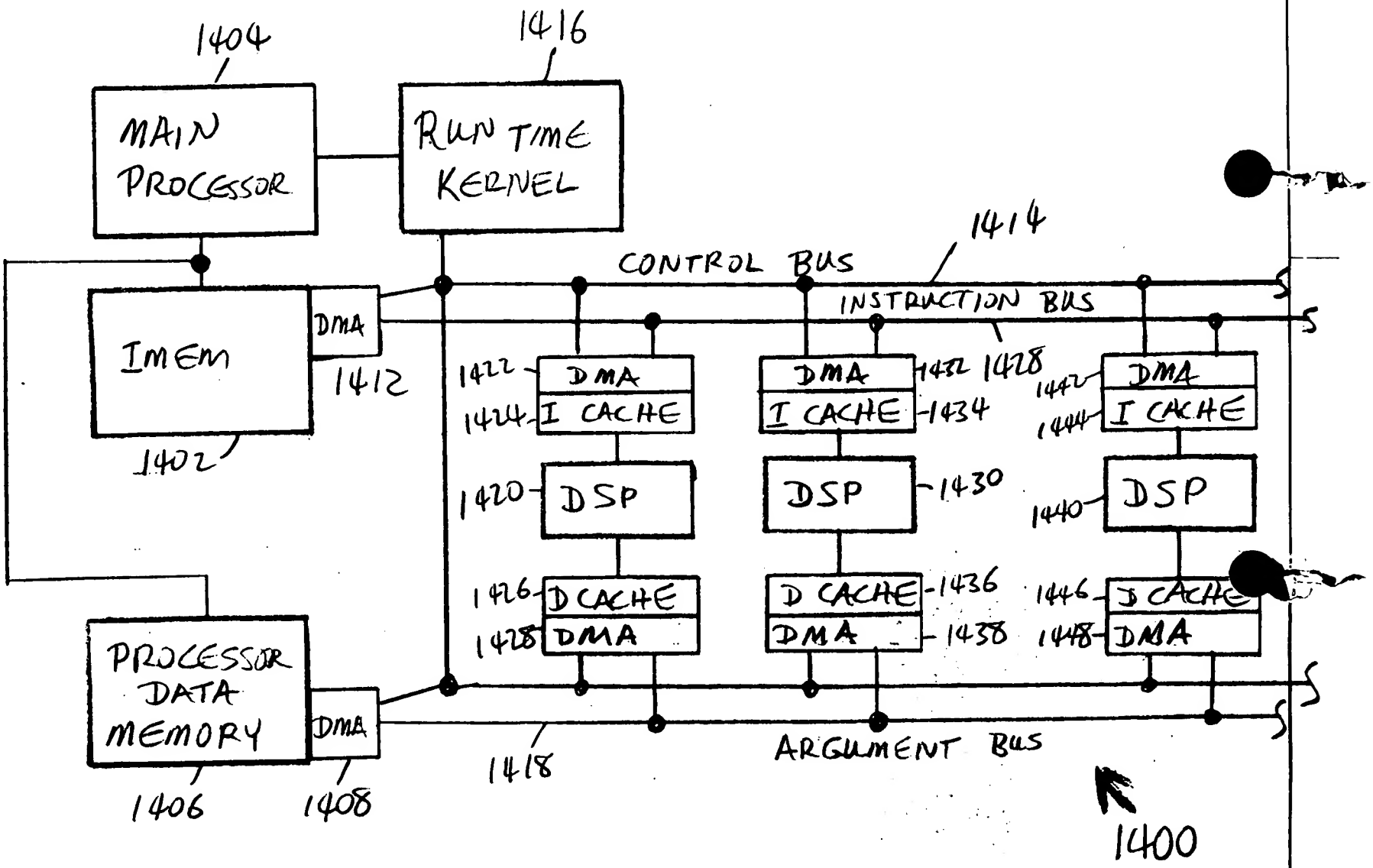

FIG 14